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Fundamentals of digital logic with Verilog design / Stephen Brown and Zvonko Vranesic — Third edition pages cm ISBN 978-0-07-338054-4 (alk paper) 1 Logic circuits—Design and construction—Data processing 2 Verilog (Computer hardware description language) 3 Computer-aided design I Vranesic, Zvonko G II Title TK7868L6B76 2014 62139 2-dc23 2012042163 ...

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